

ASSEMBLY JIG AND MANUFACTURING METHOD OF MULTILAYER SEMICONDUCTOR DEVICE
REPLACEMENT DRAWINGS
Inventor: Yoshiyuki Yanagisawa et al.
Serial No. 09/876,290
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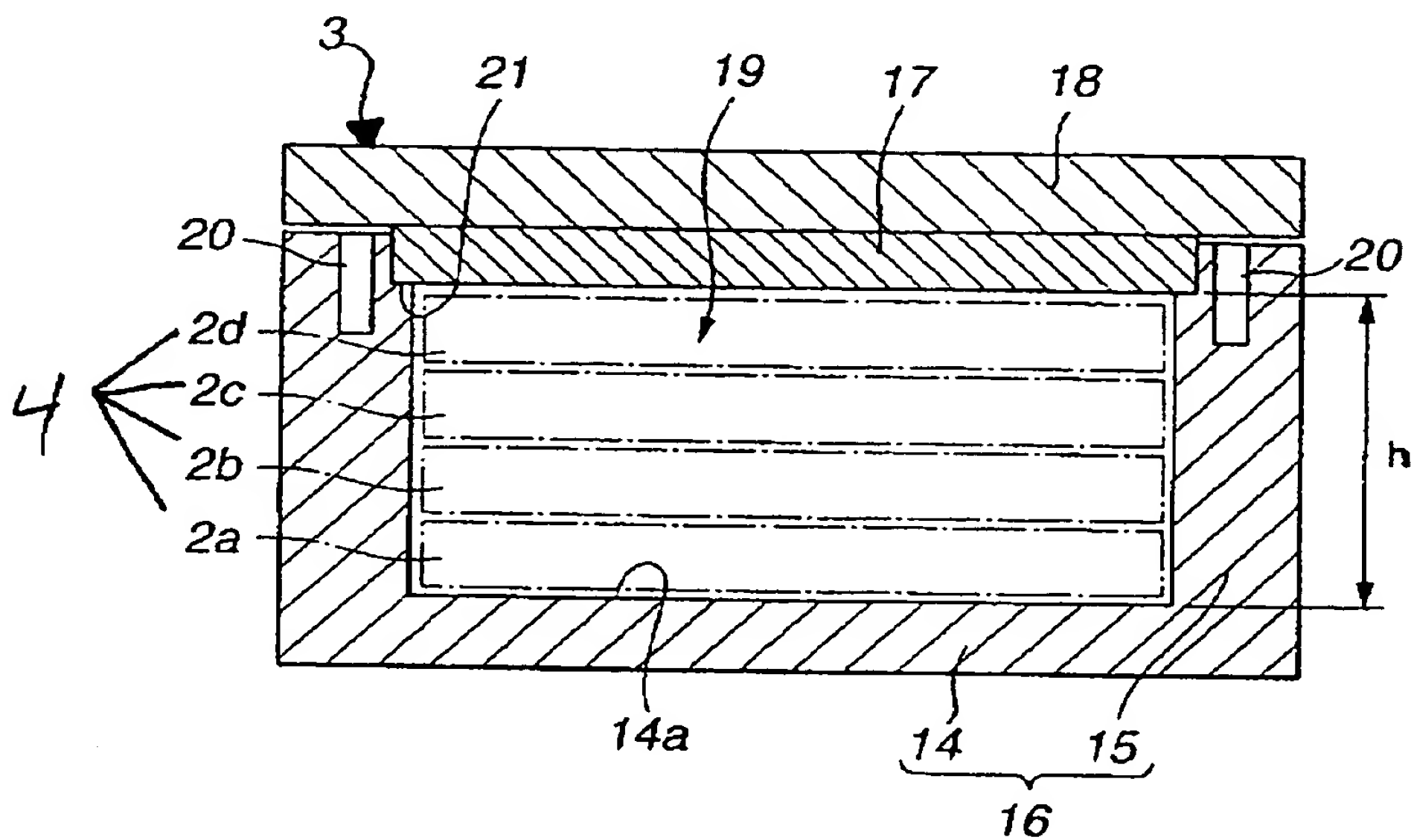


FIG. 3

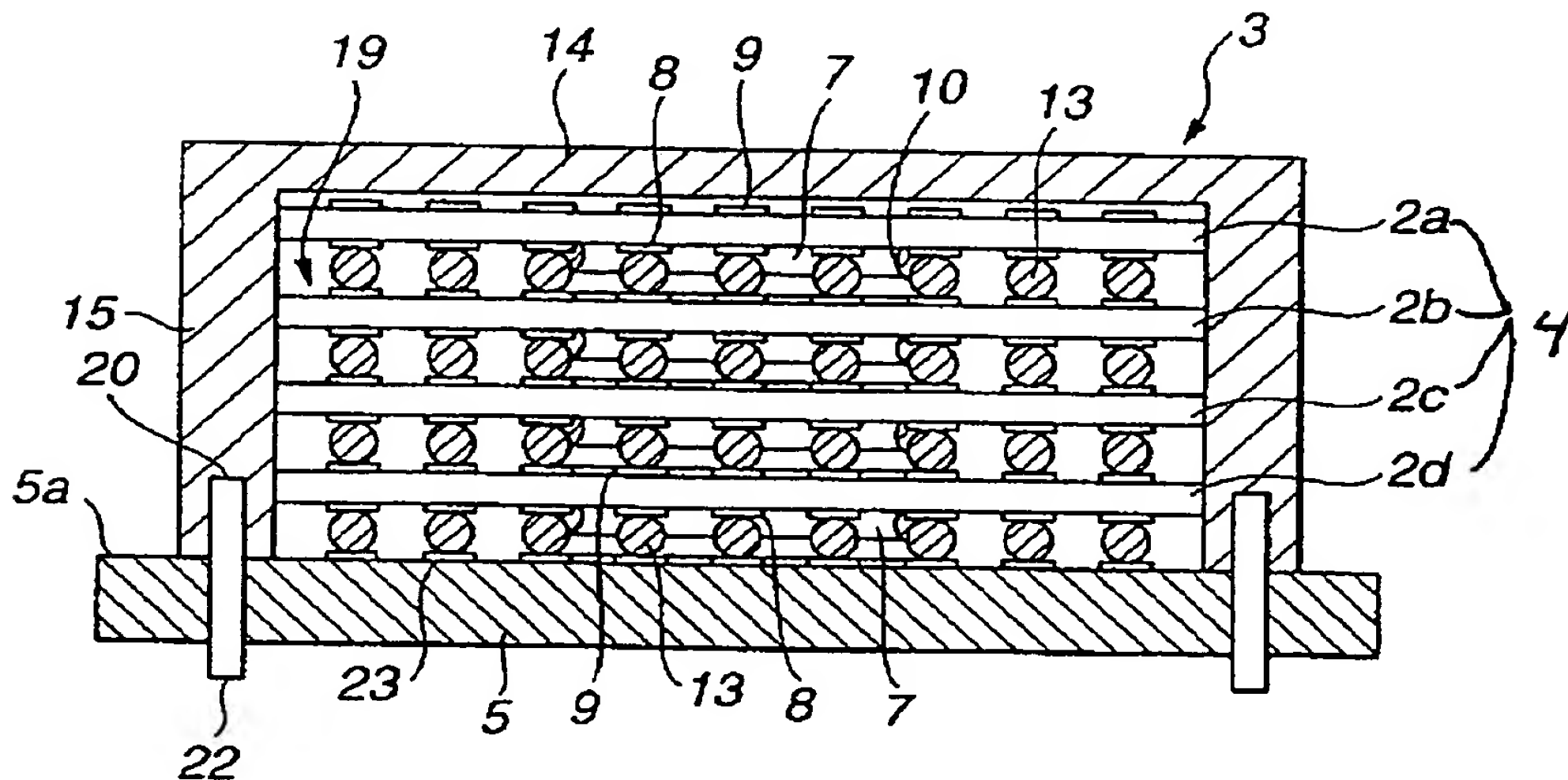


FIG. 4

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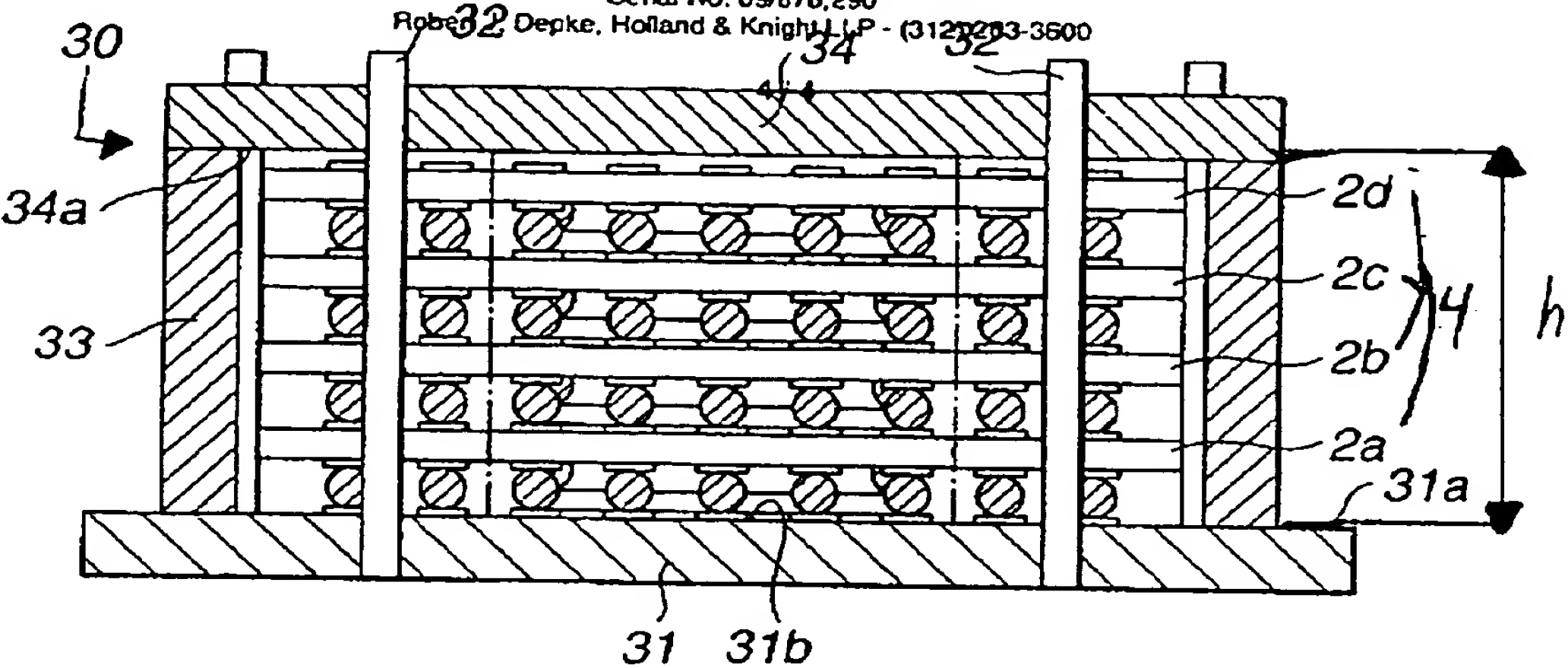


FIG. 5(a)

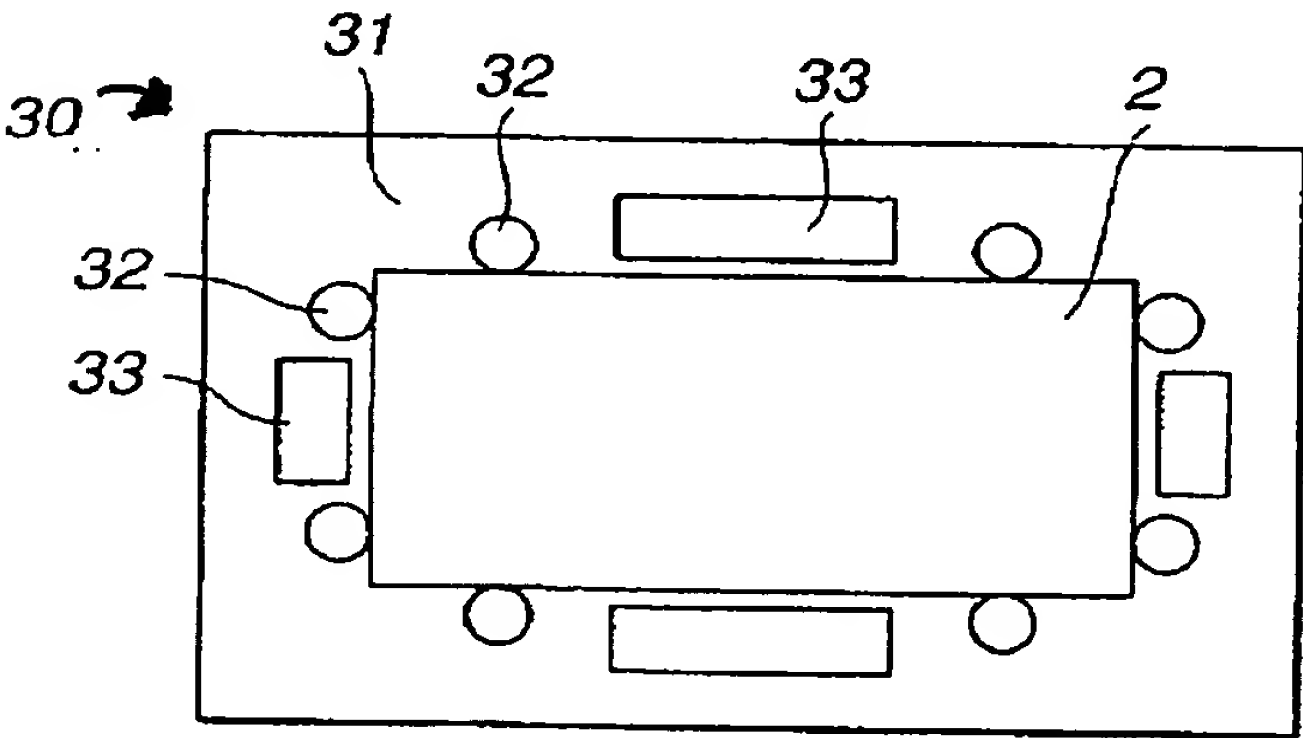


FIG. 5(b)

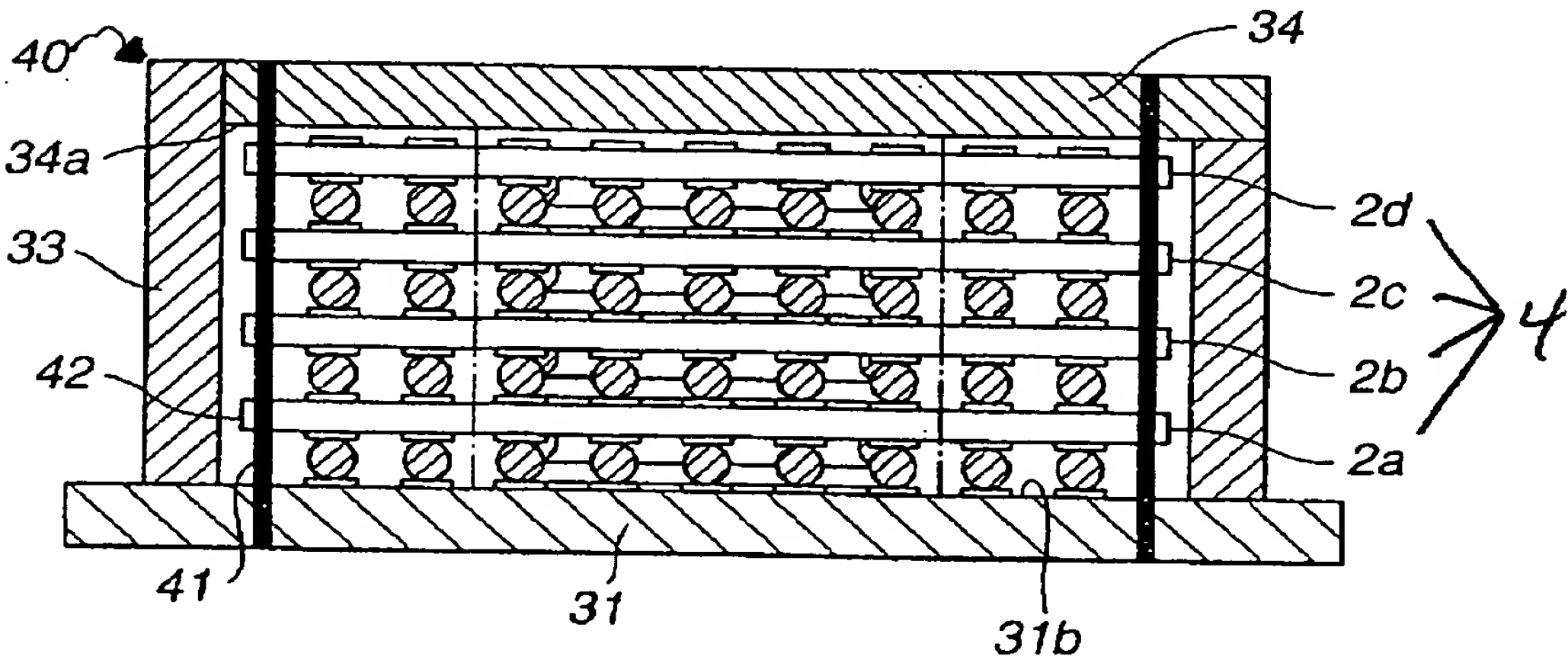


FIG. 6